



Attorney Docket No. 1381.1005 *copy*

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Shunji BABA et al.

Application No.: 09/753,570

ATTENTION

Confirmation No.: 2008

CERTIFICATE OF CORRECTION

Filed: January 4, 2001

BRANCH

U.S. Patent No.: 6,716,665

Batch No.:

Issued: April 6, 2004

For: METHOD OF MOUNTING CHIP ONTO PRINTED CIRCUIT BOARD IN SHORTENED
WORKING TIME

REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Certificate
MAY 07 2004
of Correction

Sir:

Patentee(s) respectfully request(s) that a Certificate of Correction be issued in the subject patent, pursuant to 35 U.S.C. §254 and 37 C.F.R. §1.322, to correct the mistake(s) shown on the attached Form PTO-1050.

Since the mistakes are Patent Office mistakes, it is believed that no fee is required.

Respectfully submitted,

STAAS & HALSEY LLP

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7 MAY 2004

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(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO : 6,716,665

DATED : April 6, 2004

INVENTOR(S) : Shunji BABA et al.

Col. 20, line 61, change "pick" to --nick--.

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PATENT NO. 6,716,665

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